

Electronic Acknowledgement Receipt

EFS ID:	6298967
Application Number:	10597919
International Application Number:	
Confirmation Number:	7894
Title of Invention:	Multilayer substrate with built-in-chip-type electronic component and method for manufacturing the same
First Named Inventor/Applicant Name:	Osamu Chikagawa
Customer Number:	54066
Filer:	Christopher Andrew Bennett/Michelle Rhodes
Filer Authorized By:	Christopher Andrew Bennett
Attorney Docket Number:	36856.1458
Receipt Date:	21-OCT-2009
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Application Type:	U.S. National Stage under 35 USC 371

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Miscellaneous Incoming Letter	36856-1458-PTO-20091021-CERTIFICATION.pdf	14929 e6f5c178551a5bfde8eb869a2e049d46f986af60	no	1

Warnings:

Information:

2	Translation of Foreign Priority Documents	36856-1458-PTO-20091021-Translation-of-JP2004-317313.pdf	324223 7b43fe15e937a82aeaa47a8be1978676564e1c01	no	30
Warnings:					
Information:					
3		36856-1458-PTO-20091021-Amendment.pdf	73240 95d81703b450b2d5d394c272c7385add19063da8	yes	5
	Multipart Description/PDF files in .zip description				
	Document Description		Start	End	
	Amendment/Req. Reconsideration-After Non-Final Reject		1	1	
	Claims		2	3	
	Applicant Arguments/Remarks Made in an Amendment		4	5	
Warnings:					
Information:					
Total Files Size (in bytes):			412392		
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